Form PTO-1449 (modified # 15 1005 ATTY. DKT. NO. 5181-99401 SERIAL NO. 10/601,402 List of Patents and Publications For Applicant's Information GROUP: 2171- Z/87 XPPLICANT: Cypher, et al Disclosure Statement (Use several sheets if necessary) FILING DATE: June 23, 2003 U.S. PATENT DOCUMENTS FILING DATE EXAM. REF. DATE CLASS SUB DOCUMENT NUMBER NAME APPROPRIAT **INITIALS CLASS** DES. Weber 6,209,064 3/27/01 B1 B2 6,088,768 7/11/00 Baldus, et al 5,802,582 9/1/98 Ekanadham, et al B3 5,761,721 6/2/98 **B4** Baldus, et al OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) **B5** "Specifying and Verifying a Broadcast and a Multicast Snooping Cache Coherence Protocol", Sorin, et al, IEEE Transactions on Parallel and Distributed Systems, Vol. 13, No. 6, June 2002, http://www.cs.wisc.edu/multifacet/papers/tpds02 lamport.pdf 'Multicast Snooping: A New Coherence Method Using a Multicast Address Network", Bilir, et al, The 26th International Symposium on Computer Architecture, IEEE, Atlanta, GA, May 2-4, 1999, http://csdl.computer.org/comp/proceedings/isca/1999/0170/00/01700294abs.htm "Architecture and Design of AlphaServer GS320", Gharachorloo, et al, ACM Sigplan Notices, Volume 35, **B7** Issue 11, November 2000, http://portal.acm.org/citation.cfm?id=356991&dl=ACM&coll=portal "View Caching: Efficient Software Shared Memory for Dynamic Computations", Karamcheti, et al, 11th **B8** International Parallel Processing Symposium, Geneva, Switzerland, April 1-5, 1997, http://ipdps.eece.unm.edu/1997/s13/318.pdf "Cache-Coherent Distributed Shared Memory: Perspectives on Its Development and Future Challenges", **B9** Hennessy, et al, Proceedings of the IEEE, Vol. 87, Issue 3, March 1999, ISSN 0018-9219, http://cva.stanford.edu/cs99s/papers/hennessy-cc.pdf "Survey on Cache Coherence in Shared & Distributed Memory Multiprocessors", Garg, et al, Online, B10 http://www.cse.psu.edu/~cg530/proj03/cache\_coherence.pdf **B11** "A Survey of Cache Coherence Mechanisms in Shared Memory Multiprocessors", Lawrence, Department of Computer Science, University of Manitoba, Manitoba, Canada, May 14, 1998, http://www.cs.uiowa.edu/~rlawrenc/research/Papers/cc.pdf "Bandwidth Adaptive Snooping", Martin, et al. 8th Annual International Symposium on High-Performance B12 Computer Architecture (HPCA-8), Cambridge, MA, February 2-6, 2002. "Timestamp Snooping: An Approach for Extending SMPs", Martin, et al., 9th International Conference on B13 Architectural Support for Programming Languages and Operating Systems (ASPLOS-IX), Cambridge, MA, November 13-15, 2000.

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	b.		after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus:   the certification of paragraph 2 below is provided, or  a fee of \$180.00 is enclosed.
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			deration of the following additional information (including any co-pending and oned U.S. applications, prior uses and/or sales, etc.) is requested:
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Page 1 of 1 Form PTO-1449 (Molified) ATTY. DKT. NO. 5181-99401 SERIAL NO. 10/601,402 List of Patents and Publications TRAN GROUP: 249 2/87 For Applicant's Information APPLICANT: Cypher, et al Disclosure Statement (Use several sheets if necessary) FILING DATE: June 23, 2003 U.S. PATENT DOCUMENTS REF. DOCUMENT NUMBER DATE FILING DATE EXAM. **NAME CLASS SUB** APPROPRIAT **INITIALS** DES. **CLASS** FOREIGN PATENT DOCUMENTS TRANSLATIO EXAM. REF. **DOCUMENT NUMBER** DATE COUNTRY **CLASS** SUB YES/NO INITIALS DES. **CLASS** Al 0 489 583 A2 12/4/91 **EP A2** 0 817 069 A1 6/25/97 EP OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) A3 Technical Disclosure Bulletin, "Broadcast of Mostly-Read-Only Highly Shared Cache Lines in Multiprocesso Systems", May 1987 A4 International Search Report Application #01303988.8-2212, Mailed 11/27/01

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